

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial Number	
Filing Date	July 16, 2003
Inventorship	
Applicant	
Attorney's Docket No.	356-031-USP
Title: Mathed for Chielding Intermeted Circuit Devices	

Title: Method for Shielding Integrated Circuit Devices

To: MAIL STOP ISSUE FEE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From: Thomas J. Osborne, Jr. (Tel: 720-377-0759; Fax: 720-377-0777)

Customer No. 61807

COMMENTS ON STATEMENTS OF REASONS FOR ALLOWANCE

The Applicant acknowledges with appreciation the allowance of claims 22-26 in the subject application by the Examiner. The Applicant agrees with the Examiner's Statement of Reasons for Allowance to the extent that claims 22-26 are patentable over the references in the record.

However, the Applicant expressly traverses the Examiner's Statement of Reasons for Allowance to the extent that any statement is intended to or has the intended effect of limiting a claim scope, explicitly or implicitly, by not reciting verbatim the respective claim language, or is intended to or has the effect of limiting a claim scope by stating or implying that all the reasons for patentability are in any way fully enumerated. The Applicant specifically does not acquiesce or agree in any manner as to any assertion in Examiner's statements that may be interpreted to narrow the claims to less than their recited scope.

U.S. Patent Application No. 10/621,844 Comments on Statements of Reasons for Allowance

If there are any questions, please contact the undersigned attorney.

Dated: January 31, 2007

Respectfully submitted,

Thomas J. Osborne, Jr.
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In Re U.S. Patent Application Serial No. 10/621,844
Title: Method for Shielding Integrated Circuit Devices

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Attorney Docket No. 356-031-USP

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